

		Subcla:
		Class

ISSUE CLASSIFICAT

U.S. **UTILITY** Patent Application

O.I.P.E. *PDY* PATENT DATE  
 SCANNED *10-2* O.A. *Am*

APPLICATION NO. 09/944522	CONT/PRIOR D	CLASS 438	SUBCLASS 132	ART UNIT 2812	EXAMINER Simkovic
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## APPLICANTS

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**title**

Thyristor having one or more doped layers

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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